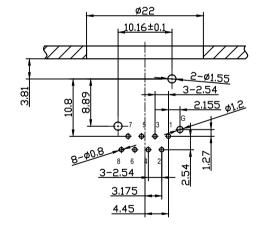
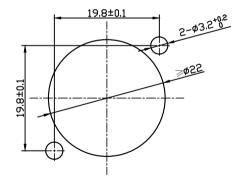


PCB-Layout (component side)
PCB hole tolerance +/-0.05:



Panel cut out (rear side):





Note: 1. All dimensions in mm.

|      |                  |      |      | THIS DOCUMENT IS THE PROPERTY OF NEUTRIK.      | Material: | Scale:      |              | Date:  | Name:      |
|------|------------------|------|------|--|-----------|-------------|--------------|--------|------------|
|      |                  |      |      | NO PART THEREOF MAY BE REPRODUCED AND USED     |           | 2:1<br>(A3) | Designer:    | ZhangJ | 2024.04.26 |
|      |                  |      |      | WITHOUT THE PRIOR WRITTEN CONSENT OF THE OWNER |           |             | Checked:     |        |            |
|      |                  |      |      |  |           |             | Approved:    |        |            |
|      |                  |      |      | Title:   |           | •           | Replaced By: |        | 1          |
|      |                  |      |      | RRE8F-A-000-0                                  |           |             |              |        | '          |
|      |                  |      |      |  |           |             | Replaces:    |        | 1          |
|      |                  |      |      |  |           |             | Drawing No.: |        |            |
| Rev. | Revision Details | Date | Name | REAN   |           |             | Diawing No.: |        |            |

1 • 1