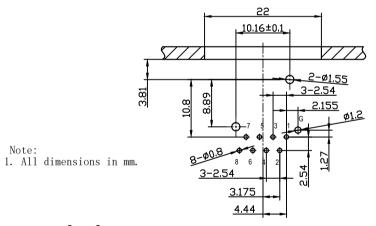
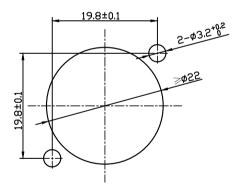


PCB-Layout (component side)
PCB hole tolerance +/-0.05:



Panel cut out (rear side):





		RRE8F-I	Replaces: Drawing No.:		1		
		Title:	Approved: Replaced By:		1		
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		THIS DOCUMENT IS THE PROPERTY OF NEUTRIK.	Material:	Scale:		Date:	Nam